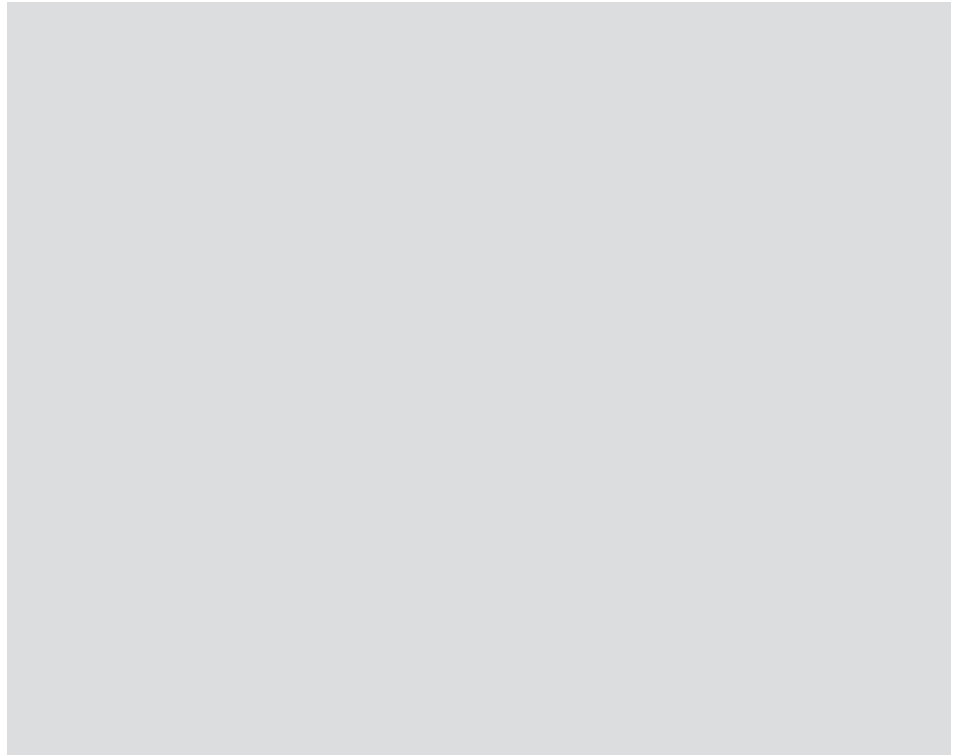


CGS70x Low Skew Clock Generators

- Up to 9 CMOS Clock Outputs
- 25-160 MHz Output Frequency Range
- Less Than 400ps Pin-to-Pin Skew
- Supports Clock Doubling and Quadrupling
- Internal Phase Locked Loop (PLL) With VCO Operating Greater Than 160 MHz
- CGS702 Has Reduced EMI
- Sub-300ps Long Term Jitter
- 30 mA Drive
- PLCC Packaging For Optimum Skew Performance

National Semiconductor's Clock Generation and Support (CGS*) family offers you a complete portfolio of clock generation and distribution timing solutions. National's newest family of high performance clock generators (CGS700V, CGS701V, CGS702V) offers you the highest performance and flexibility available in a cost-effective device. The CGS70x have on-chip PLLs that are used to generate 25-160 MHz outputs from 25-40 MHz inputs. The CGS701 has external feedback for output edge placement control.

IBIS models for the CGS70x, along with all of National's clock generators and drivers, can be downloaded (via ftp) from vhdl.org, where all of the semiconductor industry's IBIS models are located. IBIS (I/O Buffer Information Specification) is a fast and accurate behavioral method of modeling I/O buffers based on V/I curve data derived from measurements or full circuit simulation. IBIS uses a standardized software parsable format to create the behavioral information needed to model analog characteristics of ICs. IBIS can be used by almost any Analog Simulator/EDA tool in the industry. IBIS is developed by the IBIS Open Forum and most forum activities are handled through e-mail discussions using the reflector ibis@vhdl.org.



PROCESSORS SUPPORTED:
i960® Processor Family

AVAILABILITY:
Now

CONTACT:
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For international contacts see Appendix B.

